



JCO2 Rec'd PCT/PTO 03 JUN 2005  
PC #4

PATENT  
ATTORNEY DOCKET NO. 040894-7183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
	)	
Shigeki SAKAI	)	Confirmation No.: 5664
	)	
Application No.: 10/525,045	)	Group Art Unit: 2812
	)	
Filed: February 18, 2005	)	Examiner: Unassigned
	)	
For: SEMICONDUCTOR-FERROELECTRIC	)	
STORAGE DEVICES AND PROCESSES	)	
FOR PRODUCING THE SAME	)	

Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Dear Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicant brings to the attention of the Examiner the documents listed on the attached PTO Form 1449. To the best of the undersigned's knowledge, this Information Disclosure Statement is being before the mailing date of a first Office Action on the merits for the above-referenced application, and no fees are believed to be necessary.

A copy of each of the listed documents is attached for the Examiner's convenience. Applicant respectfully requests that the Examiner consider the listed documents and evidence that consideration by making the appropriate notations on the attached PTO Form 1449.

Each item of information contained in this Information Disclosure Statement (IDS) was cited on page 2 of the specification.

This submission does not represent that any better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "Prior Art." If it should be determined that any of the listed documents do not constitute "Prior Art" under United States law, Applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such documents. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 50-0310.

Respectfully submitted,

MORGAN, LEWIS & BOCKIUS LLP

Dated: June 3, 2005

By: 

Robert J. Goodell

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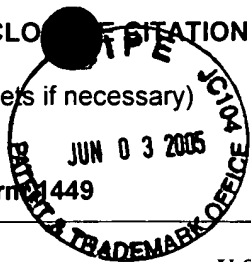
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**INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

PTO Form 1449



Attorney Docket No.  
040894-7183

Application No.:  
10/525,0452

Applicant(s): Shigeki SAKAI

PAGE 1 of 1

Filing Date: February 18, 2005

Group Art Unit: 5664

**U.S. PATENT DOCUMENTS**

*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date

**FOREIGN PATENT DOCUMENTS**

Document Number	Date	Country	Class	Sub Class	<u>Translation</u> YES NO	
JP-A-2001-291841	Oct. 19, 2001	JAPAN			abstract	
JP-A-2002-353420	Dec, 6, 2002	JAPAN			abstract	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

	S. Migita et al., "Epitaxial Structure $\text{SrBi}_2\text{Ta}_2\text{O}_9\langle 116 \rangle / \text{SrTiO}_3\langle 011 \rangle / \text{Ce}_{0.12}\text{Zr}_{0.88}\langle 001 \rangle / \text{Si}\langle 001 \rangle$ for Ferroelectric-Gate FET Memory," <i>Integrated Ferroelectrics</i> , Vol. 40, pp. 2135-143 (2001).
	Shinji Migita et al., "Fabrication and Electrical Properties of Ferroelectric-gate FET with Epitaxial Gate Structure," <i>Denshi Jōhō Tūushin Gakkai Ronbun-shi</i> , Vol. J85-C, No. 1 (January 2002 issue), pp. 14-22.

Examiner	Date Considered
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.